



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231  
[www.uspto.gov](http://www.uspto.gov)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/192,651	11/16/1998	TIMOTHY W. FUEHRER	2-9-24-11	1068

7590 03/21/2002

MARK D SIMPSON  
SYNNESTVEDT & LECHNER  
2600 ARAMARK TOWER  
1101 MARKET STREET  
PHILADELPHIA, PA 191072950

EXAMINER

SINGH, RAMNANDAN P

ART UNIT

PAPER NUMBER

2644

DATE MAILED: 03/21/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Advisory Action</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	09/192,651	FUEHRER ET AL.
	<b>Examiner</b>	<b>Art Unit</b>
	Dr. Ramnandan Singh	2644

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 05 March 2002 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

- a)  The period for reply expires 3 months from the mailing date of the final rejection.
- b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1.  A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2.  The proposed amendment(s) will not be entered because:
  - (a)  they raise new issues that would require further consideration and/or search (see NOTE below);
  - (b)  they raise the issue of new matter (see Note below);
  - (c)  they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d)  they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_.

3.  Applicant's reply has overcome the following rejection(s): \_\_\_\_.
4.  Newly proposed or amended claim(s) \_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5.  The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6.  The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7.  For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_.

Claim(s) objected to: \_\_\_\_.

Claim(s) rejected: \_\_\_\_.

Claim(s) withdrawn from consideration: \_\_\_\_.

8.  The proposed drawing correction filed on \_\_\_\_ is a) approved or b) disapproved by the Examiner.
9.  Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s). \_\_\_\_.
10.  Other: \_\_\_\_

Continuation Sheet 5, does NOT place the application in condition for allowance because of the following:

Applicant has amended claims 15 and 18. The new limitation has already been considered in the prior Office actions. To emphasize this point, Hein et al [US 6,198,816 B1] teaches a preferred embodiment for a clock recovery circuit 216 for use in this invention is detailed in FIG. 5 and described below.:

One section of the clock recovery circuit may be a phase locked loop ("PLL") circuit, consisting of phase/frequency detector 531, charge pump 532, resistor 533, capacitor 534, and voltage controlled oscillator ("VCO") 535. The other section of the clock recovery block is data latch 542 operating outside the phase locked loop to re-time the digital data received across the isolation barrier. Circuitry for performing these functions is well known to those skilled in the art. See, for example, F. Gardner, Phaselock Techniques, 2d ed., John Wiley & Sons, NY, 1979; and R. Best, Phase-Locked Loops, McGraw-Hill, 1984, which are incorporated herein by reference. The data input to the receive system from the isolation capacitors may be derived from a differential signal present at the barrier by passing the differential signal through MOS input buffers (not shown), which are well known in the art, and providing a single-ended binary output signal 530 to the clock recovery circuit [col. 12, lines 59-67; col. 13, lines 1-10].

Clearly, Hein et al discloses a combination of cloak and charge pump for line powered DAA.

*Elam-R.D.*  
3/19/02

*Forester*  
FORESTER W. ISEN  
FORESTER W. ISEN  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2000